



Improving the Performance in Delta Sigma Modulator Transmitter

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ARTICLE INFO	ABSTRACT
<p>Article History: Received 9 April 2018 Received in revised form 2 August 2018 Accepted 21 September 2018 Available online 22 September 2018</p>	<p>One significant limitation associated with delta-sigma modulator (DSM)-based transmitters is the substantial quantization noise generated at the output of the DSM, which negatively impacts the overall efficiency of the transmitter. This quantization noise arises due to the finite resolution of the digital-to-analog conversion process, leading to unwanted distortion and signal degradation. As a result, the performance of DSM-based transmitters is often limited by the trade-off between quantization noise and signal quality, specifically the signal-to-noise and distortion ratio (SNDR). This paper addresses the challenge of improving the efficiency of DSM-based transmitters by proposing a novel technique aimed at reducing quantization noise while maintaining a high SNDR. The focus of this approach is on enhancing the overall performance of DSM systems, without compromising the quality of the transmitted signal. The proposed technique involves advanced signal processing methods that selectively mitigate the effects of quantization noise, thereby improving the efficiency of the transmission process. Simulation results demonstrate the effectiveness of the proposed method, particularly in the context of a 1.92 MHz Long Term Evolution (LTE) input signal, which is commonly used in modern wireless communication systems. For an oversampling ratio (OSR) of 16, the coding efficiency (CE) of the DSM-based transmitter is significantly improved, rising from 9.7% to 23%. At the same time, the SNDR remains high, measuring approximately 41 dB, indicating that the quality of the transmitted signal is preserved. These results suggest that the proposed technique offers a promising solution to enhance the performance of DSM-based transmitters, making them more efficient without sacrificing signal quality.</p>
<p>Keywords: Switch Mode Power Amplifier (SMPA), Delta Sigma Modulator (DSM)</p>	

1. INTRODUCTION

The Delta Sigma Modulator (DSM)-transmitter is a transmitter architecture designed to generate signals capable of efficiently driving power amplifiers (PAs) and switch-mode power amplifiers (SMPAs) [1-6]. In the DSM-transmitter context, a non-constant envelope input signal is converted into a constant envelope signal [7, 8]. This

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transformation allows the PA to operate without power back-off, thus maximizing its efficiency. Additionally, the constant envelope signal effectively drives high-efficiency switched-mode power amplifiers (SMPAs) [9]. Due to their high reconfigurability, DSMs are ideal for software-defined radio (SDR) applications [10-12, 6].

Several architectures have been proposed for DSM-transmitters. In [13-15], a band-pass DSM is used to quantize the RF signal for driving the SMPA. However, this quantization is performed at a high carrier frequency, which presents significant challenges in RF applications operating at gigahertz frequencies. Alternatively, in [10, 16, 17], a low-pass DSM is utilized to quantize the baseband signal. The main issue with this approach is the substantial quantization noise at the DSM output, which results in low coding efficiency (CE) and, consequently, poor power efficiency in the DSM-transmitter [17].

Efforts have been made in [18, 19] to reduce quantization noise at the DSM output by using a multi-bit low-pass DSM. However, in these cases, the DSM output signal does not maintain a constant envelope, making it unsuitable for driving PAs or SMPAs.

To address this, a polar transmitter configuration featuring a low-pass envelope DSM (LPEDSM) in an envelope elimination and restoration (EER) setup is proposed in [20-22]. In this design, the quantized envelope of the signal, produced by the DSM, controls the drain voltage supply of the SMPA. Nonetheless, switching a large DC-supply current on and off remains challenging, particularly when high output power is required [14].

This paper enhances DSM-transmitter efficiency by employing a signal processing technique that reduces quantization noise at the DSM output. The proposed technique improves coding efficiency (CE) while preserving the signal-to-noise and distortion ratio (SNDR) of the DSM-transmitter.

The structure of the paper is as follows: Section 2 introduces the DSM-transmitter architecture. In Section 3, we explain how the efficiency of the DSM-transmitter is improved by mitigating quantization noise. Section 4 presents the simulation results for the proposed quantization noise reduction method. Finally, Section 5 concludes the paper.

2. ARTICHECTURE OF DSM-TRANSMITTER

The architecture of the DSM-Transmitter is depicted in Figure 1. Initially, the quadrature components of the baseband input signal (I, Q) undergo quantization through the utilization of 1-bit DSMs. The quantized quadrature components exhibit two distinct levels, namely -1 and 1. The envelope and phase of the quantized signal at the output of DSMs can be expressed as follows [23]:

$$|S_q| = \sqrt{I_q^2 + Q_q^2} = \sqrt{2} \tag{1}$$

And

$$\angle S_q = \tan^{-1}\left(\frac{I_q}{Q_q}\right) = \frac{\pi}{4} \cdot \frac{3\pi}{4} \cdot \frac{5\pi}{4} \cdot \frac{7\pi}{4} \tag{2}$$

Here, I_q and Q_q are quantized quadrature components and S_q is the quantized signal at the output of DSMs. as can be seen in (1) this quantized signal has constant envelope with the magnitude of $\sqrt{2}$. Therefore, this signal is an ideal signal for PA and SMPA in order to have a good linearity and efficiency. Then this quantized signal is up converted to desire frequency in order to drive SMPA and at last a bandpass filter is used to remove the quantization noise. Two metrics are used in order to evaluate the quantization noise effect on DSM-transmitter performance: CE and SNDR. CE is the ratio of the signal power to the total power at the output of DSM (Figure 2). Since the quantization noise has a significant role in a transmitter’s efficiency, this parameter is used to evaluate the effect of the DSM quantization noise on the transmitter’s efficiency. CE can be defined as [17, 23]:

Two metrics are employed to assess the impact of quantization noise on DSM-transmitter performance: Coding Efficiency (CE) and Signal-to-Noise and Distortion Ratio (SNDR). CE represents the ratio of signal power to the total power at the output of DSM (Figure 2). Given the significant role of quantization noise in a transmitter's efficiency, CE serves as a crucial parameter for evaluating the effect of DSM quantization noise on the transmitter's efficiency. CE is defined as follows [17, 23]:

$$CE = \frac{\text{SignalPower}}{\text{TotalPower}(\text{SignalPower} + \text{QuantizarionNoisePower})} \quad (3)$$

In fact, the CE shows how much of the quantized signal power at the output of the DSM belongs to desired signal. SNDR is another metric to study the linearity performance of the delta – sigma-based transmitter and can be defined as [11, 23]:

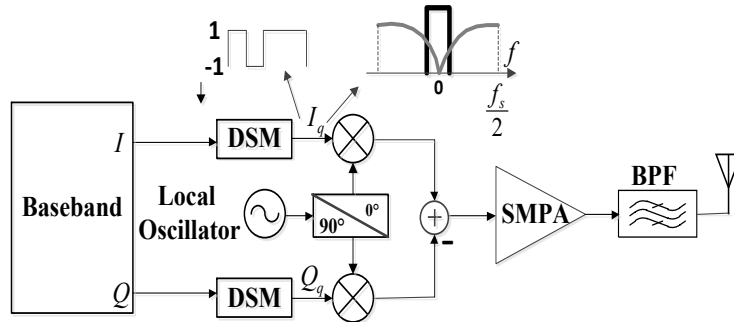


Fig. 1. Architecture of DSM-transmitter.

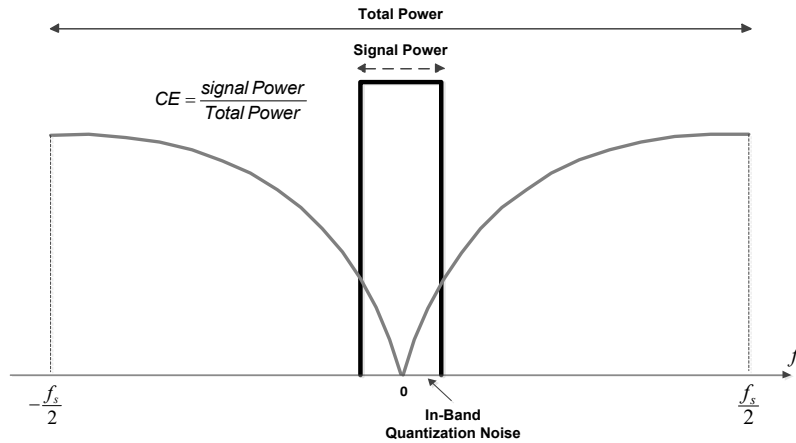


Fig. 2. Definition of CE.

$$SNDR = \frac{\text{SignalPower}}{\text{In-Band Noise and Distortion Power}} \quad (4)$$

Note that, unlike the CE, for calculating SNDR value both DSM quantization noise and PA distortion should be considered.

The overall efficiency of DSM based transmitter is estimated as [21, 23]:

$$\eta_T = CE \times \eta_{PA} \quad (5)$$

Where η_T is the overall transmitter efficiency and η_{PA} is the PA efficiency. By using ideal SMPA with theoretically 100% efficiency [1], (5) can be expressed as:

$$\eta_T = CE \quad (6)$$

We can conclude from (6) CE has the main role in determining the transmitter efficiency. So, improving CE has a significant effect on improving of overall transmitter efficiency.

3. PERFORMANCE IMPROVEMENT IN DSM-TRANSMITTER

The proposed method for reducing quantization noise and improving Coding Efficiency (CE) in DSM-transmitter is illustrated in Figure 3. As depicted, the process initiates with subtracting the input signal from the quantized signal to isolate the quantization noise. Subsequently, a low-pass filter is employed to distinguish the in-band quantization noise from the overall quantization noise. Following this, the in-band quantization noise is subtracted from the quantized signal to obtain a signal devoid of in-band quantization noise, and from the total quantization noise to acquire out-of-band quantization noise. The out-of-band quantization noise is then attenuated by a quantization noise factor, α , and subtracted from the in-band noise-free quantized signal.

In essence, this method achieves complete elimination of in-band quantization noise while reducing out-of-band quantization noise. The elimination of in-band quantization noise has minimal impact on the DSM output signal due to its small magnitude. However, the reduction in out-of-band quantization noise can introduce fluctuations in the DSM's output signal. Therefore, the quantized signal no longer maintains a constant envelope, potentially causing distortion in the Switch-Mode Power Amplifier (SMPA). Nevertheless, by controlling the noise reduction level, the signal variation can be kept sufficiently small to ensure SMPA distortion remains below the DSM quantization noise. Consequently, the overall Signal-to-Noise and Distortion Ratio (SNDR) is preserved, while Coding Efficiency experiences a significant improvement.

The elimination of in-band quantization noise contributes to enhanced SNDR in the DSM-transmitter, allowing for more effective reduction of out-of-band quantization noise and improvement in CE compared to scenarios where only out-of-band quantization noise reduction is applied to the DSM-transmitter. It is crucial to control the quantization noise reduction level to maintain reasonable envelope fluctuation and uphold signal quality, as exceeding a specific threshold can lead to increased envelope variation and substantial degradation in SNDR.

Note that, β in Figure 3 is coding efficiency improvement factor and can be expressed as follow:

$$\beta = \frac{\text{QuantizationNoisePowerbeforeNoiseReduction}}{\text{QuantizationNoisePowerafterNoiseReduction}} \quad (7)$$

In the next section, the simulation for proposed architecture in Figure 3 will be performed.

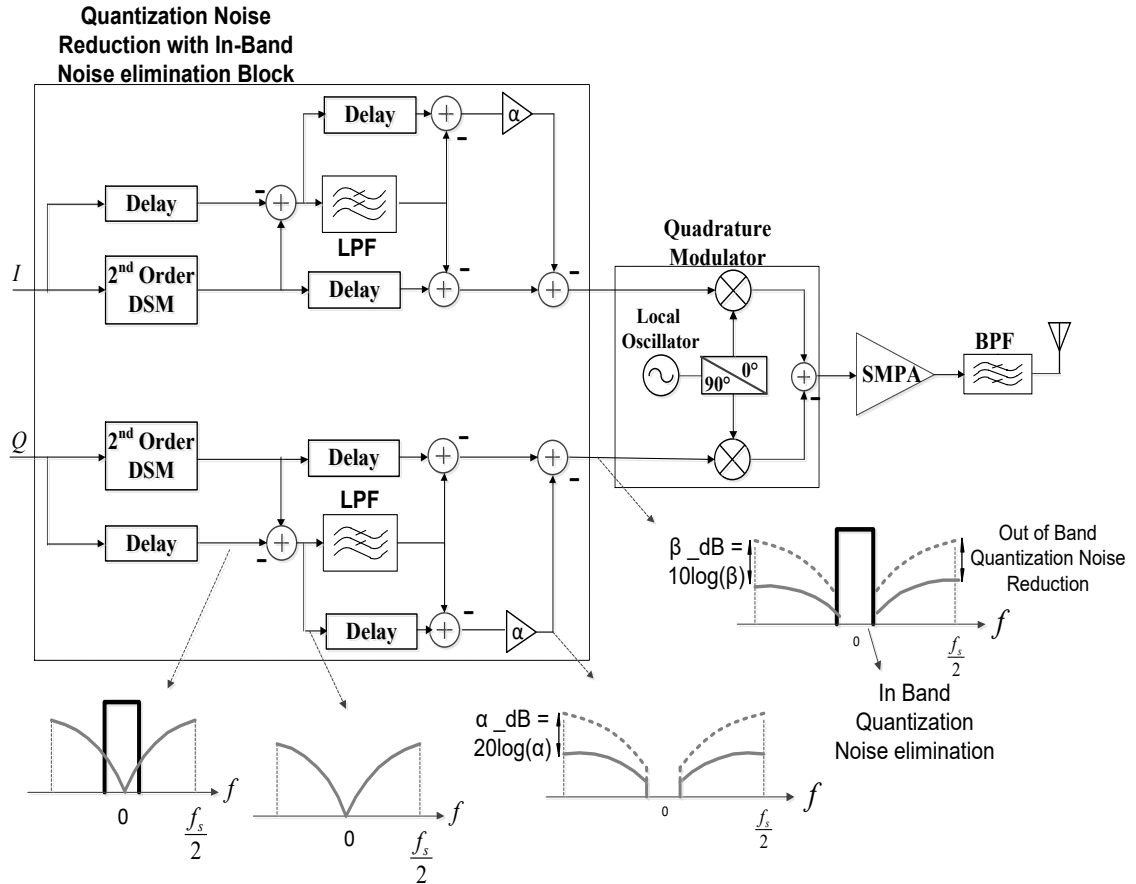


Fig. 3. Quantization noise reduction with in-band noise elimination in DSM- transmitter.

4. SIMULATION RESULTS

To determine the optimal amount of quantization noise reduction that does not compromise signal quality, the quantization noise reduction with in-band noise elimination block in Figure 3 was implemented in MATLAB Simulink. Subsequently, various quantization noise reduction factors (α) were applied to this block, and the resulting outputs were utilized as the input to the Switch-Mode Power Amplifier (SMPA).

The process involved importing the output signals of the quantization noise reduction block with different noise reduction factors, obtained from MATLAB, into the Advanced Design System (ADS). The RF modulator block in ADS was then employed for frequency up conversion and signal amplification to feed the SMPA. The SMPA chosen for this study was the inverse class F (F-1), designed based on the methodology outlined in [24]. The GaN transistor (CGH40010 from Cree Inc.), nonlinear model provided by the manufacturer, biased at $V_{DS}=28$ V and $V_{GS}=-3$ V, was utilized for the SMPA design. This inverse class F SMPA exhibited a maximum output power of 39.6 dBm, a power-added efficiency of 78%, and a power gain of 13.5 dB at 850 MHz.

The output signal from the inverse class F SMPA was then re-imported into MATLAB to calculate the Signal-to-Noise and Distortion Ratio (SNDR). This iterative process allowed for the assessment of different quantization noise reduction levels and their impact on the SMPA output signal quality.

The results of the quantization noise reduction with in-band noise elimination technique for an Uplink Long-Term Evolution (LTE) signal with a 1.92 MHz bandwidth and an oversampling ratio (OSR) of 16, for different quantization noise reduction factors, are presented in Figure 4 and Table 1. It is evident that utilizing quantization noise reduction with in-band quantization noise elimination for the Uplink LTE signal with 16 OSR improves Coding Efficiency (CE) from 9.7% to 23% (for $\alpha=0.38$), while maintaining a signal quality of around 41 dB. Furthermore,

Figure 4 and Table 1 indicate that this CE improvement ranges from 9.7% to 20% (for $\alpha=0.34$) when only quantization noise reduction technique (without in-band quantization noise elimination) is employed. Therefore, by incorporating in-band quantization noise elimination along with quantization noise reduction technique, we can further enhance CE while maintaining the same Signal-to-Noise and Distortion Ratio (SNDR) (41 dB).

The magnitude of the Delta Sigma Modulator (DSM) output signal before and after quantization noise reduction with in-band quantization noise elimination for $\alpha=0.38$ is displayed in Figure 5. The plot illustrates that quantization noise reduction introduces fluctuations in the signal magnitude. However, as long as the value of α remains below 0.38, this signal variation does not adversely impact signal quality, and the SNDR remains above 41 dB.

Additionally, Figure 6 depicts the spectrum of the Switch-Mode Power Amplifier (SMPA) output signal for the Uplink LTE signal with a 1.92 MHz bandwidth and OSR of 16 under various conditions, including without quantization noise reduction ($\alpha=0$), and using quantization noise reduction with in-band noise elimination for $\alpha=0.3$.

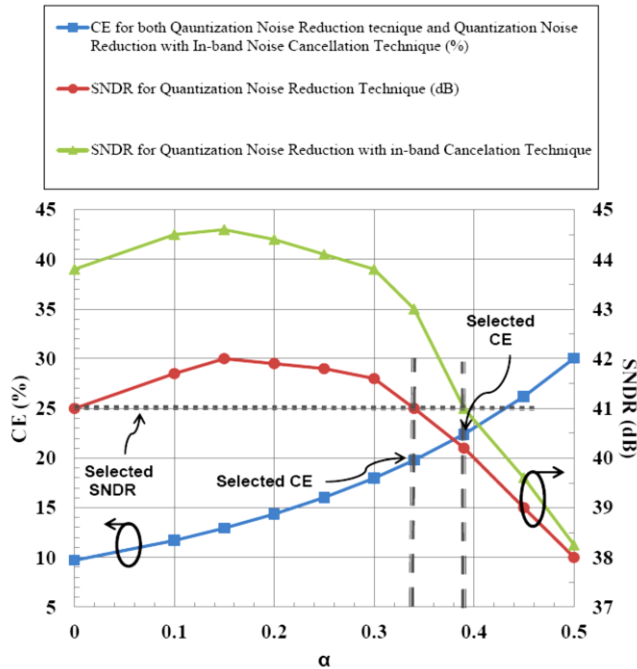


Fig. 4. Results of quantization noise reduction and quantization noise reduction with in-band quantization noise elimination for an Uplink LTE signal with 1.92 MHz bandwidth and an OSR of 16.

Table 1. The SNDR and CE results of quantization noise reduction and quantization noise reduction with in-band noise elimination for an Uplink LTE signal with 1.92 MHz bandwidth and an OSR of 16.

Signal	BW(MHz)	OSR	Method	CE(%)	SNDR(dB)
LTE	1.92	16	Without quantization noise reduction	9.7	41
			Quantization noise reduction ($\alpha=0.34$)	20	41
			Quantization noise reduction with in-band noise elimination ($\alpha=0.38$)	23	41

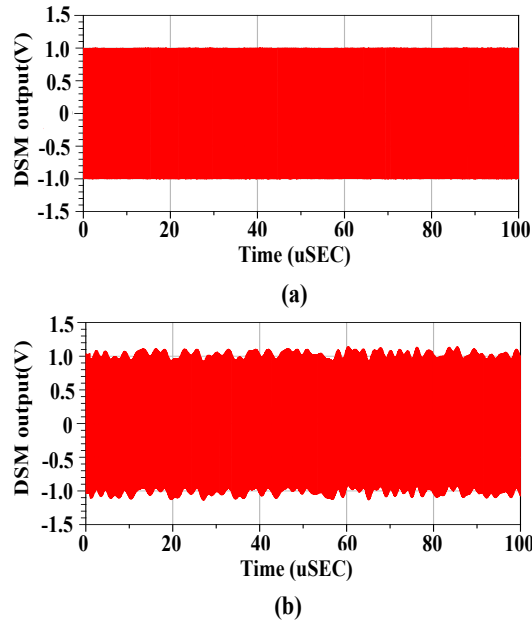


Fig. 5. Magnitude of DSM output signal (a) before quantization noise reduction (b)after quantization noise reduction with in-band quantization noise elimination for $\alpha=0.38$

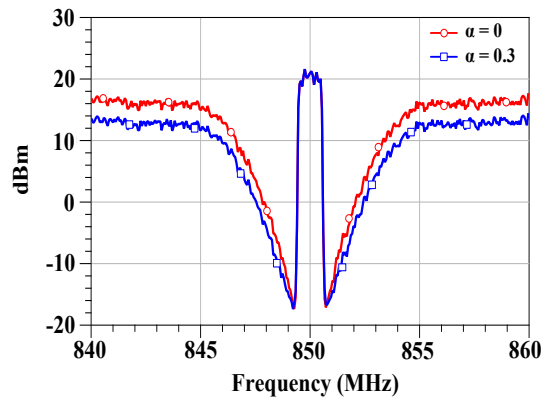


Fig. 6. The spectrum of the SMPA output signal for the Uplink LTE signal with 1.92 MHz bandwidth and OSR of 16 without quantization noise reduction, $\alpha=0$, and using quantization noise reduction with in-band noise elimination for $\alpha = 0.3$.

5. CONCLUSION

In this study, the application of quantization noise reduction with in-band quantization noise elimination to the Delta Sigma Modulator (DSM)-transmitter was explored to enhance transmitter efficiency. While this technique introduced fluctuations in the output DSM signal, careful control of the noise reduction level ensured that the signal variation remained sufficiently small to prevent Switch-Mode Power Amplifier (SMPA) distortion from surpassing the DSM quantization noise. As a result, the Signal-to-Noise and Distortion Ratio (SNDR) was successfully maintained.

Simulation results demonstrated that employing this technique for a 1.92 MHz Long-Term Evolution (LTE) input signal with an Oversampling Ratio (OSR) of 16 led to a notable improvement in the Coding Efficiency (CE) of the DSM. The CE increased from 9.7% to 23%, while maintaining an SNDR of around 41 dB. This outcome suggests the effectiveness of the quantization noise reduction with in-band quantization noise elimination approach in optimizing DSM-transmitter performance.

Transparency Statement

The data supporting this study are available upon reasonable request to the corresponding author, subject to ethical and confidentiality considerations.

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Declaration of Interest

The authors declare that they have no competing interests.

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REFERENCES

- [1] Marks, N., Kong, W., & Birt, Daniel S.. (2018). Stability of a Switched Mode Power Amplifier Interface for Power Hardware-in-the-Loop. *IEEE Transactions on Industrial Electronics*, 65, 8445-8454 . <http://doi.org/10.1109/TIE.2018.2814011>
- [2] Sharma, T., Aflaki, Pouya., Helaoui, M., & Ghannouchi, F.. (2018). Broadband GaN Class-E Power Amplifier for Load Modulated Delta Sigma and 5G Transmitter Applications. *IEEE Access*, 6, 4709-4719 . <http://doi.org/10.1109/ACCESS.2017.2789248>
- [3] Bhat, R., Chakrabarti, A., & Krishnaswamy, H.. (2015). Large-Scale Power Combining and Mixed-Signal Linearizing Architectures for Watt-Class mmWave CMOS Power Amplifiers. *IEEE Transactions on Microwave Theory and Techniques*, 63, 703-718 . <http://doi.org/10.1109/TMTT.2014.2387055>
- [4] Lee, Ilseop., Kim, Byoung-ho., & Lee, Byung-geun. (2016). A Low-Power Incremental Delta–Sigma ADC for CMOS Image Sensors. *IEEE Transactions on Circuits and Systems II: Express Briefs*, 63, 371-375 . <http://doi.org/10.1109/TCSII.2015.2503706>
- [5] Liu, Y., Pan, Wensheng., Shao, S., & Tang, Youxi. (2015). A General Digital Predistortion Architecture Using Constrained Feedback Bandwidth for Wideband Power Amplifiers. *IEEE Transactions on Microwave Theory and Techniques*, 63, 1544-1555 . <http://doi.org/10.1109/TMTT.2015.2416184>
- [6] Barton, T., & Perreault, D.. (2014). Four-Way Microstrip-Based Power Combining for Microwave Outphasing Power Amplifiers. *IEEE Transactions on Circuits and Systems I: Regular Papers*, 61, 2987-2998 . <http://doi.org/10.1109/TCSI.2014.2321203>
Grebennikov, A. (2011). *RF and Microwave Transmitters Design*, 1st ed. USA: Wiley., 848 p. (In English). ISBN 978-0-470-52099-4. <https://doi.org/10.1002/9780470929308>
- [7] Ebrahimi, M. M., Helaoui, M. and Ghannouchi, F. M. (2009). Efficiency Enhancement of a WiMAX Switching Mode GaN Power Amplifier Trough Layout Optimization of Distributed Harmonic Matching Network. *Proceedings of the IEEE European Microwave Conf, Rome, Sep 29-Oct 1.2009*. <https://doi.org/10.23919/EUMC.2009.5296184>
- [8] Eron, M., Kim, B., Raab, F., Caverly, R. and Staudinger, J. (2011). The Head of the Class. *IEEE Microwave Mag*, vol. 12, no. 7, p. S17-S33. <https://doi.org/10.1109/MMM.2011.942725>
- [9] Helaoui, M., Hatami, S., Negra, R. and Ghannouchi, F. M. (2008). A Novel Architecture of delta-sigma modulator Enabling All-Digital Multiband Multistandard RF Transmitters Design. *IEEE Trans. Circuits Syst II*, vol. 55, no. 11, p. 1129-1133. <https://doi.org/10.1109/TCSII.2008.2003345>

- [10] Ebrahimi, M. M., Helaoui, M. and Ghannouchi, F. M. (2011). Time-Interleaved Delta Sigma Modulator for Wideband Digital GHz Transmitters Design and SDR Applications. *J. Progr. Electromagn. Res. B.*, vol. 34, p. 263-281. <https://doi.org/10.2528/PIERB11071205>
- [11] Moallemi, S. and Jannesari, A. (2012). The Design of Reconfigurable Delta-Sigma Modulator for Software Defined Radio Applications. *Proceedings of the IEEE Int. Conf. Circuits Syst*, Kuala Lumpur, Oct 3.-4.2012. <https://doi.org/10.1109/ICCircuitsAndSystems.2012.6408301>
- [12] Keyzer, J. S., Hinrichs, J. M., Metzger, A. G., Lwamoto, M., Galton, I. and Asbeck, P. M. (2001). Digital Generation of RF Signal for Wireless Communications with Bandpass Delta Sigma Modulation. *Proceedings of the IEEE Int. Microwave Symp. Dig.*, Phoenix, May 20.-24.2001.
- [13] Hung, T. P., Rode, J., Larson, L. E. and Asbeck, P. M. (2007). Design of H-bridge Class-D Power Amplifiers for Digital Pulse Modulation Transmitters. *IEEE Trans. Microw Theory Techn.*, vol. 55, no. 12, p. 2845-2855. <https://doi.org/10.1109/TMTT.2007.909881>
- [14] Johnson, T. and Stapleton, S. P. (2006). RF Class-D Amplification with Bandpass Sigma-Delta Modulator Drive Signals. *IEEE Trans. Circuits Syst I.*, vol. 53, no. 12, p. 2507-2520. <https://doi.org/10.1109/TCSI.2006.885980>
- [15] Ebrahimi, M. M. and Helaoui, M. (2013). Reducing Quantization Noise to Boost Efficiency and Signal Bandwidth in Delta-Sigma-Based Transmitters. *IEEE Trans. Microw Theory Techn.*, vol. 61, no. 12, p. 4245-4250. <https://doi.org/10.1109/TMTT.2013.2288702>
- [16] Ghannouchi, F. M., Hatami, S., Aflaki, P., Helaoui, M. and Negra, R. (2010). Accurate Power Efficiency Estimation of GHz Wireless Delta-Sigma Transmitters for Different Classes of Switching Mode Power Amplifiers. *IEEE Trans. Microw Theory Techn.*, vol. 58, no. 11, p. 2812-2819. <https://doi.org/10.1109/TMTT.2010.2077552>
- [17] Ebrahimi, M. M., Bassam, S. A., Helaoui, M. and Ghannouchi, F. M. (2011). Feedback-Based Digital Predistorter for Multi-Bit Delta-Sigma Transmitter. *Proceedings of the IEEE MWSCAS*, Seoul, Aug 7.-10.2011. <https://doi.org/10.1109/MWSCAS.2011.6026540>
- [18] Chung, S. W. and Dawson, J. L. (2011). Digital Predistortion Using Quadrature $\Delta\Sigma$ Modulation with Fast Adaptation for WLAN Power Amplifier. *Proceedings of the IEEE MTT-S Int. Microw. Symp. Dig.*, Baltimore, June 5.-10.2011. <https://doi.org/10.1109/MWSYM.2011.5973438>
- [19] Moon, J., Son, J., Lee, J. and Kim, B. (2011). A Multimode/Multiband Envelope Tracking Transmitter with Broadband Standard Amplifier *Proceedings of 10th International Conference on Telecommunication in Modern Satellite Cable and Broadcasting Services (TELSIKS)*, Nis, Oct 5.-8.2011.
- [20] Choi, J. et al. (2007). A $\Delta\Sigma$ Digitized Polar RF Transmitter. *IEEE Trans. Microw Theory Techn.*, vol. 55, no. 12, p. 2679-2690. <https://doi.org/10.1109/TMTT.2007.907137>
- [21] Shameli, A. et al. (2008). A Two-Point Modulation Technique for CMOS Power Amplifier in Polar Transmitter Architecture. *IEEE Trans. Microw Theory Techn.*, vol. 56, no. 1, p. 31-38. <https://doi.org/10.1109/TMTT.2007.912012>
- [22] Ebrahimi, M. M., Helaoui, M. and Ghannouchi, F. M.. (2013). Delta-Sigma-Based Transmitters: Advantages and Disadvantage. *IEEE Microwave Mag.*, vol. 14, no. 1, p. 68-78. <https://doi.org/10.1109/MMM.2012.2226541>
- [23] Grebennikov, A. (2011). A High-Efficiency 100-W Four-Stage Doherty GaN HEMT Power Amplifier Module for WCDMA Systems. *Proceedings of the IEEE MTT-S Int. Microw. Symp. Dig.*, Baltimore, June 5.-10.2011.

<https://doi.org/10.1109/MWSYM.2011.5972568>

- [24] Lee, S. K., Cho, Y. H., & Kim, S. H. (2010). Collaborative filtering with ordinal scale-based implicit ratings for mobile music recommendations. *Information Sciences*, 180(11), 2142–2155. <https://doi.org/10.1016/j.ins.2010.02.004>